

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated August 16, 2007 has been received and its contents carefully reviewed.

By this Amendment, claim 10 is cancelled. Claims 1, 6, 11-13 and 16-17 are amended. No new matter is added. Claims 1-2, 4-6, 8-9 and 11-18 are currently pending in the present application. Reexamination and reconsideration of the pending claims is respectfully requested.

In the Office Action, the Examiner rejected claims 1-18 under 35 U.S.C. § 102(e) as being anticipated by Miyazawa (U.S. Patent No. 6,858,991). This rejection is respectfully traversed and reconsideration is requested. Applicant notes that because claims 3 and 7 were cancelled in the amendments filed dated May 1, 2007, Applicant considers this rejection relates to claims 1-2, 4-6 and 8-18.

With respect to claim 10, this rejection is rendered moot in view of cancellation of the claim at issue.

Claim 1 is allowable over the cited references in that each of these claim 1 recites a combination of elements including, for example, "...a power control driver supplying a power voltage to a source terminal of the first driving thin film transistor through the power line, the power voltage having an On value during an emitting time section of a single frame and an Off value during a rest time section of the single frame, wherein the first driving thin film transistor is turned on when the On value and the Off value are applied to the organic electroluminescent diode during the emitting time section and the rest time section, respectively." Claim 6 is allowable over the cited references in that each of these claim 6 recites a combination of elements including, for example, "...a power control driver supplying a power voltage to the power line, the power voltage having an On value during an emitting time section of a single frame and an Off value during a rest time section of the single frame, wherein the driving thin film transistor is turned on when the On value and the Off value are applied to the organic electroluminescent diode during the emitting time section and the rest time section, respectively..." Claim 13 is allowable over the cited references in that each of these claim 13 recites a combination of elements including, for example, "...applying an On value of a power voltage through a source terminal of the driving transistor to an organic electroluminescent diode

that is connected to a drain terminal of the driving transistor during an emitting time section of a single frame; applying an Off value of the power voltage through the source terminal of the driving transistor to the organic electroluminescent diode during a rest time section of the single frame, wherein the driving thin film transistor is turned on when the On value and the Off value are applied to the organic electroluminescent diode during the emitting time section and the rest time section, respectively.”

Miyazawa teaches that the source terminal of the driving transistor Trd is connected to the second power line L2 connected to the voltage-supply line VL supplying a constant power voltage Vdd. See column 15, lines 61-65 and FIG. 8. That is, the power voltage supplied to the source terminal of the driving transistor Trd has a constant value.

Further, Miyazawa teaches at FIG. 9 that the electrical current I_{ds} does not flows through the driving transistor Trd during the data writing period and the electrical current I_{ds} flows the driving transistor Trd during the emitting period. That is, the driving transistor Trd is turned off during the data writing period and the driving transistor Trd is turned on during the emitting period.

Further, the Examiner states at Response to Arguments of page 7 of this Office Action that “Miyazawa discloses the power control driver supplies first value (waveform I_{ds}) during an emitting time section of the single frame (TC) and a second value during the rest of the time.” However, the power control driver 150 merely supplies a power-line control signal to a power-line control line F to turn on/off the transistor Q. See column 15, lines 30-33 and FIGs. 7 and 8. That is, the power-line control signal is not supplied to the source terminal of the driving transistor Trd.

For at least this reason, Miyazawa fails to teach or suggest at least above feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 1 and its dependent claims 2 and 4-5, claim 6 and its dependent claims 8-9, 11-12 and 17, and claim 13 and its dependent claims 14-16 and 18 are allowable over the cited references.

Applicant believes the application is in condition for allowance and early, favorable action is respectfully solicited. If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-

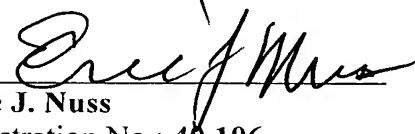
7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Dated: 16 November 2007

Respectfully submitted,

By



Eric J. Nuss

Registration No.: 40,106

McKENNA LONG & ALDRIDGE LLP

1900 K Street, N.W.

Washington, DC 20006

(202) 496-7500

Attorneys for Applicant